

Fig.1

Title: Heterojunction Organic Semiconductor Field Effect Transistor (FET) with a Gate Insulation Layer and Manufacturing Process Thereof

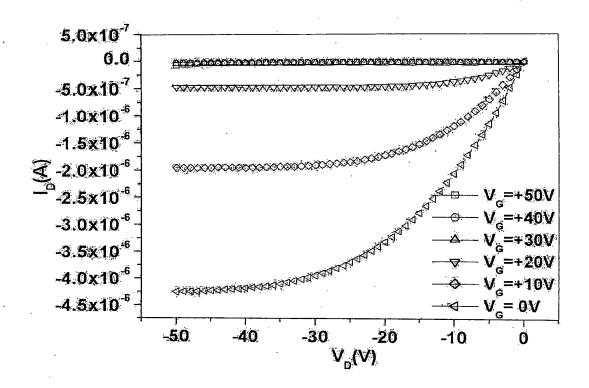


Fig.2

Title: Heterojunction Organic Semiconductor Field
Effect Transistor (FET) with a Gate Insulation
Layer and Manufacturing Process Thereof

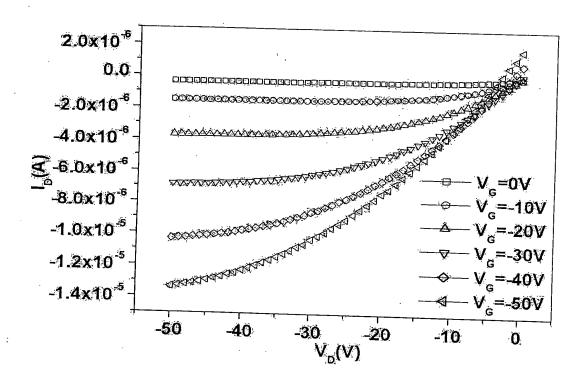


Fig.3

Title: Heterojunction Organic Semiconductor Field Effect Transistor (FET) with a Gate Insulation Layer and Manufacturing Process Thereof

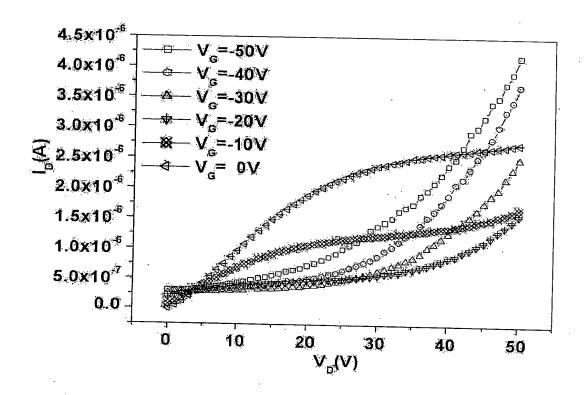


Fig.4

Title: Heterojunction Organic Semiconductor Field
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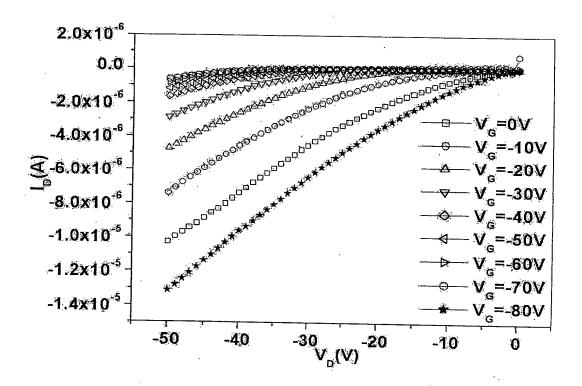


Fig.5

Title: Heterojunction Organic Semiconductor Field Effect Transistor (FET) with a Gate Insulation Layer and Manufacturing Process Thereof